

Modeling Microprocessor Performance focuses on the development of a design and evaluation tool, named RIPE (Rensselaer Interconnect Performance Estimator). This tool analyzes the impact on wireability, clock frequency, power dissipation, and the reliability of single chip CMOS microprocessors as a function of interconnect, device, circuit, design and architectural parameters. It can accurately predict the overall performance of existing microprocessor systems. For the three major microprocessor architectures, DEC, PowerPC and Intel, the results have shown agreement within 10% on key parameters. The models cover a broad range of issues that relate to the implementation and performance of single chip CMOS microprocessors. The book contains a detailed discussion of the various models and the underlying assumptions based on actual design practices. As such, RIPE and its models provide an insightful tool into single chip microprocessor design and its performance aspects. At the same time, it provides design and process engineers with the capability to model, evaluate, compare and optimize single chip microprocessor systems using advanced technology and design techniques at an early design stage without costly and time consuming implementation. RIPE and its models demonstrate the factors which must be considered when estimating tradeoffs in device and interconnect technology and architecture design on microprocessor performance.

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Modeling and Analyzing CPU Power and Performance: Metrics, Methods, and Abstractions. Margaret Martonosi. David Brooks. Pradip Bose. D E I. M.

Performance Analysis Tools. • Workloads and Workload Analysis. • CPU Modeling. • System Modeling. • Power Modeling • brief outline. In this context, performance models play a crucial role because developers use them to study design alternatives and predict the performance of processors and . combined with architectural or system level performance simulation is a key modeling. In all of these tools, microprocessor power is estimated by accruing.

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